	ED STATES PATENT	and Trademark Office	UNITED STATES DEPAR United States Patent and Address: COMMISSIONER F P.O. Box 1450 Alexandria, Virginia 223 www.uspto.gov	OR PATENTS
APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/517,541	03/02/2000	Kia Silverbrook	AUTH15US	4978
7590 06/22/2004			EXAMINER	
Kia Silverbrook Silverbrook Research Pty Ltd			NGUYEN, NGA B	
393 Darling Street			ART UNIT	PAPER NUMBER
Balmain, 204			3628	
AUSTRALIA			DATE MAILED: 06/22/200	4

Please find below and/or attached an Office communication concerning this application or proceeding.

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	09/517,541	SILVERBROOK ET AL.	
Office Action Summary	Examiner	Art Unit	
	Nga B. Nguyen	3628	144
The MAILING DATE of this communication			address
Period for Reply			
<ul> <li>A SHORTENED STATUTORY PERIOD FOR RETINE MAILING DATE OF THIS COMMUNICATION</li> <li>Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication</li> <li>If the period for reply specified above, the maximum statutory provide the reply within the set or extended period for reply will, by set any reply received by the Office later than three months after the rearned patent term adjustment. See 37 CFR 1.704(b).</li> </ul>	ON. FR 1.136(a). In no event, however, may a in n. a reply within the statutory minimum of thir eriod will apply and will expire SIX (6) MON statute, cause the application to become AB	reply be timely filed ty (30) days will be considered ti ITHS from the mailing date of thi 3ANDONED (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed on $\underline{0}$	<u>01 April 2004</u> .		
	This action is non-final.		
3) Since this application is in condition for all	owance except for formal mat	ers, prosecution as to	the merits is
closed in accordance with the practice unc	der <i>Ex parte Quayle</i> , 1935 C.D	). 11, 453 O.G. 213.	
isposition of Claims			
4)	ion		
4a) Of the above claim(s) is/are with			
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>1-8</u> is/are rejected.			
7) Claim(s) is/are objected to.			
8) Claim(s) are subject to restriction a	nd/or election requirement.		
Application Papers			
9) The specification is objected to by the Exar	miner.		
10) The drawing(s) filed on is/are: a)	accepted or b) objected to	by the Examiner.	
Applicant may not request that any objection to	the drawing(s) be held in abeyar	nce. See 37 CFR 1.85(a)	
Replacement drawing sheet(s) including the co	prrection is required if the drawing	(s) is objected to. See 37	CFR 1.121(d).
11) The oath or declaration is objected to by th	e Examiner. Note the attached	d Office Action or form	PTO-152.
riority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for for	eian priority under 35 U.S.C. 8	119(a)-(d)  or (f)	
a) All b) Some * c) None of:			
1. Certified copies of the priority docum	nents have been received.		
2. Certified copies of the priority docum		pplication No.	
3. Copies of the certified copies of the			al Stage
application from the International Bu			-
* See the attached detailed Office action for a	list of the certified copies not	received.	
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ttachment(s)	<b></b>		
) 🖄 Notice of References Cited (PTO-892) ) 🔲 Notice of Draftsperson's Patent Drawing Review (PTO-948	4) [_] Interview S Paper Note	Summary (PTO-413) S)/Mail Date	
) Information Disclosure Statement(s) (PTO-1449 or PTO/SE	·	nformal Patent Application (F	DTO 152)
Paper No(s)/Mail Date	6) 🗌 Other:		-10-152)

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## **DETAILED ACTION**

1. This Office Action is the answer to the communication filed on April 1, 2004,

which paper has been placed of record in the file.

2. Claims 1-8 are pending in this application.

## **Response to Arguments/Amendment**

3. Applicant's arguments with respect to claims 1-8 have been considered but are

moot in view of new grounds of rejection.

## Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

5. Claims 1-8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Park,

U.S. Patent No. 5,673,223.

Regarding to claim 1, Park discloses in an authentication chip in which secret

data is manipulated, a method of shielding manipulations of the secret data from

observation, including the steps of: operating non-flashing CMOS structures in the chip,

in which pMOS and nMOS transistors are driven such that they do not have

intermediate resistance simultaneously during a change of state of the CMOS structure,

to manipulate the secret data (columns 1-2 and column 4, lines 17-55).

Park does not disclose operating conventional CMOS inverters adjacent the nonflashing CMOS structures at the same time. However, operating the conventional CMOS inverters is well known in the art. Therefore, it would have been obvious to one with ordinary skill in the art at the time the invention was made to modify Park's to operate the conventional CMOS inverters the same time with the non-flashing CMOS structures for the purpose of improving the security in operation of an authentication chip in which secret data is manipulated

Regarding to claims 2-3, Park does not disclose generating continuous circuit noise to a tamper detection line and driving the conventional CMOS structures from the tamper detection line. However, generating continuous circuit noise to a tamper detection line and driving the conventional CMOS structures from the tamper detection line is well known in the art of semiconductor. Therefore, it would have been obvious to one with ordinary skill in the art at the time the invention was made to modify Park's to include the feature above for the purpose of generating continuous circuit noise in the conventional CMOS structures.

Regarding to claim 4, Park does not disclose driving the conventional CMOS multiple times faster than non-flashing CMOS. However, it is well known in the art to drive the conventional CMOS multiple times faster than non-flashing CMOS. This is a desired choice. Therefore, it would have been obvious to one with ordinary skill in the art at the time the invention was made to modify Park's to include the feature above for the desired purpose.

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Regarding to claims 5-8, Park discloses an authentication chip (figures 2-3) for performing the method as discussed in claims 1-4 above, therefore are rejected by the same rationale.

## Conclusion

6. Claims 1-8 are rejected.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to examiner Nga B. Nguyen whose telephone number is (703) 306-2901. The examiner can normally be reached on Monday-Thursday from 9:00AM-6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hyung S. Sough can be reached on (703) 308-0505.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 306-1113.

8. Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

C/o Technology Center 3600

Washington, DC 20231

Or faxed to:

(703) 872-9326 (for formal communication intended for entry),

or

(703) 308-3691 (for informal or draft communication, please label

"PROPOSED" or "DRAFT").

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Hand-delivered responses should be brought to Crystal Park 5, 2451 Crystal Drive, Arlington, VA, Seventh Floor (Receptionist).

Nga B. Nguyen Maanguyen June 10, 2004